SONET-SDH

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Today telephone network is largely based on the evolution of the first digital infrastructure, based on a TDM system, with strict synchronization requirements, or PDH – Plesiochronous Digital Hierarchy:

- **SONET** – Synchronous Optical NETwork (optical signal, based rate of 51.84Mbit/s)
- **SDH** – Synchronous Digital Hierarchy (European standard equivalent to SONET)
- **STS** – Synchronous Transport Signal (equivalent standard for electric signals)
Plesiochronous Digital Hierarchy (PDH) is the original standard for telephone network, now abandoned in favor of SONET/SDH

- Exploits Time Division Multiplexing
- Designed to support digital voice channels at 64kb/s
- No store and forward: imposes strict synchronization between TX and RX. A “plesio-synchronous” solution is adopted (almost synchronous)
- Different standard in US/EU/Japan
  - Make it difficult to connect different networks
### $T-, E$- Hierarchy

<table>
<thead>
<tr>
<th>Level</th>
<th>US (T-)</th>
<th>Europe (E-)</th>
<th>Japan</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>0.064 Mb/s</td>
<td>0.064 Mb/s</td>
<td>0.064 Mb/s</td>
</tr>
<tr>
<td>1</td>
<td>1.544 Mb/s</td>
<td>2.048 Mb/s</td>
<td>1.544 Mb/s</td>
</tr>
<tr>
<td>2</td>
<td>6.312 Mb/s</td>
<td>8.488 Mb/s</td>
<td>6.312 Mb/s</td>
</tr>
<tr>
<td>3</td>
<td>44.736 Mb/s</td>
<td>34.368 Mb/s</td>
<td>32.064 Mb/s</td>
</tr>
<tr>
<td>4</td>
<td>274.176 Mb/s</td>
<td>139.264 Mb/s</td>
<td>97.928 Mb/s</td>
</tr>
</tbody>
</table>
**T-1 carrier system: US standard**

- 24-voice channels are the results of voice sampling, quantization, and coding using the PCM standard and TDM framing.
- Additional signaling channel of 1 bit.
- T1 speed is \((24 \times 8 + 1) \times 8000 = 1.544\text{Mb/s}\).

A sample every \(125\mu\text{sec}\).
A frame every \(125\mu\text{sec}\).

193 bits per frame.

More frames can be multiplexed (TDM) on faster channels.
64 \times 24 + 8k = 1.544 \text{ Mb/s}

T1 Frame transmitted over a DS1

4 \text{ DS1} = 1 \text{ DS2}
4 \times 1.544 = 6.312 \text{ Mb/s}

7 \text{ DS2} = 1 \text{ DS3}
7 \times 6.312 = 44.736 \text{ Mb/s}

6 \text{ DS3} = 1 \text{ DS4}
6 \times 44.736 = 274.176 \text{ Mb/s}

It is difficult to access a single channel in a high-speed stream: de-multiplexing of all tributaries must be performed.

It is difficult to have perfect synchronization between ALL nodes. “bit stuffing” to overcome this.
**PDH**

Digital transmission system (T-carrier, E-carrier) exploiting TDM to multiplex lower speed streams into higher speed channel.

Every apparatus has its own clock. No network-wide synchronization is possible.

Every clock is different, and therefore synchronization errors show up.

Solution: insert (and remove) stuffing bits in the frame (but stuffing).
PDH - Sincronizzazione

Frame

Bit Stuffing

Source

Node

Dest

Faster clock
**PDH - Synchronization**

- **Positive Stuffing:**
  - Data are written in a temporary buffer.
  - Data are read from the buffer with a higher rate to transmit data to the (faster) transmission channel.
  - Every time the buffer is going to be empty, stuffing bit is transmitted instead of real data.
  - Stuffing MUST be signaled to the receiver, so that stuffing bits can be removed.
- A different frame is used at the data layer and at the transmission layer! This makes mux/demux operation much more complex.
**PDH drawbacks**

Lack of efficiency: it is hard to extract slower tributaries from the high speed aggregate

Lack of flexibility:
- No monitoring standard
- No management standard

Lack of “mid-fiber meet”
- No common physical standard – every manufacturer has its own (no NNI standard)
From PDH to SONET/SDH

SONET: Synchronous Optical Network: American standard
SDH: Synchronous Digital Hierarchy: EU and Japan standard
Standardization occurred in ’80
Telecom Providers drove the standardization process
  - PDH system was not anymore scalable, and did not guarantee to support traffic growth
  - Optical technologies were becoming commonly used, foreseeing bandwidth increase
  - Interoperability among different providers were a nightmare.
What is SONET/SDH

Set of ITU-T recommendation (first from 1989):

- Define a structured multiplexing hierarchy
- Define management and protection mechanisms
- Define physical layer requirements (optical components)
- Define multiplexing of different sources and protocols over SONET/SDH
Main goals of SONET/SDH:

- Fault tolerance of telecom providers requirement (99.999% - five nines - availability)
- Interoperability among different manufacturers
- Flexibility of upper layer formats to adapt to different source (not only voice)
- Complex monitoring capabilities of performance and of traffic (50 ms of recovery time)
## SONET/SDH hierarchy

<table>
<thead>
<tr>
<th>OC level</th>
<th>STS level</th>
<th>SDH level</th>
<th>Mbit/s</th>
</tr>
</thead>
<tbody>
<tr>
<td>OC-1</td>
<td>STS-1</td>
<td></td>
<td>51.84</td>
</tr>
<tr>
<td>OC-3</td>
<td>STS-3</td>
<td>STM-1</td>
<td>155.52</td>
</tr>
<tr>
<td>OC-12</td>
<td>STS-12</td>
<td>STM-4</td>
<td>622.08</td>
</tr>
<tr>
<td>OC-24</td>
<td>STS-24</td>
<td>STM-8</td>
<td>1244.16</td>
</tr>
<tr>
<td>OC-48</td>
<td>STS-48</td>
<td>STM-16</td>
<td>2488.32</td>
</tr>
<tr>
<td>OC-192</td>
<td>STS-192</td>
<td>STM-64</td>
<td>9953.28</td>
</tr>
<tr>
<td>OC-768</td>
<td>STS-768</td>
<td>STM-256</td>
<td>39813.12</td>
</tr>
<tr>
<td>OC-3072</td>
<td>STS-3072</td>
<td>STM-1024</td>
<td>159252.48</td>
</tr>
</tbody>
</table>
SONET/SDH reference model

Path layer (close to OSI layer 3 - Network)
- Manages end-to-end connection
- Monitoring and management of user connection

Line Layer
- Multiplexing of several path-layer connection among nodes
- Protection and Fault Management

Section Layer
- Define regenerator functions
- SONET’s Line and Section layers are almost equivalent to 2 (Data Link) OSI layer

Photonic Layer (same as OSI layer 1)
- Defines all the transmission requirements of signals.
Layering in SONET/SDH standard ITU-T G.78x
SONET Physical Layer

SONET physical layer is strongly optically-centric
More important recommendations are:

- **ITU-T G.957**: Optical interfaces for equipments and systems relating to the synchronous digital hierarchy
  - Single span, single channel link without optical amplifiers
- **ITU-T G.691**: Optical interfaces for single-channel STM-64, STM-256 and other SDH systems with optical amplifiers
  - Single channel, single or multi span, optically amplified links at 622 Mbit/s, 2.5 Gbit/s, 10 Gbit/s
- **ITU-T G.692**: Optical interfaces for multichannel systems with optical amplifiers
  - Multi channel, single or multi span, optically amplified
  - Definition of the ITU frequency grid

Large variety of possibilities, from very short-haul interoffice links up to a ultra-long haul, WDM backbone links

- All physical parameter of all interfaces are defined
SONET Framing

SONET/SDH TX send a continuous, synchronous streams of bit a given rate.

Multiplexing of different tributaries is performed by the means of TDM.

Apparently complex, but the TDM scheme has been designed to simplify the VLSI implementation.

A SONET frame is a very organized stream of bits:

- At a given multiplexing level, every tributary becomes a Synchronous Payload Envelope (SPE).
- Some bits, the Path Overhead, are added to the SPE, to implement monitoring, management, control functions.
- SPE + Path Overhead are a PDU, called Virtual Tributary (VT).
**STS-1 framing**

1 frame = 810 Byte in 125μs

Transport Overhead:
- 0 µs (1st bit)
- 3 rows
- 6 rows

Payload:
- 3 Bytes
- 87 Bytes

Path Overhead:
- Is removed only at the path layer
- 125 µs (last bit)
SONET: Framing STS-1

- 90 byte payload
- 3 byte framing
- 87 byte payload

Section overhead: A1, A2, C1
Line overhead: B1, B3, E1, F1, C2, G1, F2, H4
Path overhead: D1, D2, D3, D4, D5, D6, D7, D8, D9, D10, D11, D12, Z1, Z2, E2, H2, H3, H4, Z3, Z4, Z5

0 μs to 125 μs time
**STS-1 frame**

- **SOH**
- **LOH**
- **SP**
- **SPE**

Frame #1

Frame #2

Frame #3

- 810 Bytes/frame
- 8 bit/sample
- 810 samples/frame

- 9x90 Bytes/frame
- 8000 frame/second
- 8 bit/Byte

- 51,840 Mb/s

SPE of frame N can end in frame N+1
Higher layer multiplexing

STSI MUX

Byte interleave

3x3 3x87
Virtual Tributary (VT)

VTs are identified by pointers along the frame. Pointers are stored in the line overhead.

- A pointer states where a VT begins in the frame.

A recursive approach is allowed: a VT may multiplex other smaller VTs.

This allows to multiplex contributing tributaries running at very different speed in an efficient manner.
SONET hierarchy

A sample of SONET hierarchy

- SONET has been designed to support a very large set of technologies: IP, ATM, PDH, ...

Lower speed tributaries are multiplexed by PDH

- DS1 (1.544 Mb/s) to VT1.5
- E1 (2.048 Mb/s) to VT2
- DSIC (3.152 Mb/s) to VT3
- DS2 (6.3122 Mb/s) to VT6

Higher speed tributaries:
- DS3 (44.736 Mb/s)
- ATM (48.384 Mb/s)
- E4 (139.264 Mb/s)
- ATM (149.760 Mb/s)

Multiplexing:
- Byte interleaved multiplexing

Output:
- SPE STS-1
- STS-1
- SPE STS-3c
- STS-3c
- STS-N
SONET Overheads

Different overhead
- **Section**: used and managed by two section equipment
- **Line**: to allow signaling among STS multiplexers
- **Path**: end-to-end, added to the SPE to transform it in a VT

Each exploit a different function
- **Multiplexing**
- **Management**
- **Allocation of resources**
SONET overhead

Section Overhead:

- Generated and removed by Section Terminal Equipments (STE)
- Monitoring of the section performance
- Operation, administration and maintenance (OAM) voice channel
- Framing
<table>
<thead>
<tr>
<th>Section Overhead</th>
<th>A1</th>
<th>A2</th>
<th>J0/Z0</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>B1</td>
<td>E1</td>
<td>F1</td>
</tr>
<tr>
<td>2</td>
<td>D1</td>
<td>D2</td>
<td>D3</td>
</tr>
<tr>
<td>3</td>
<td>H1</td>
<td>H2</td>
<td>H3</td>
</tr>
<tr>
<td>4</td>
<td>B2</td>
<td>K1</td>
<td>K2</td>
</tr>
<tr>
<td>5</td>
<td>D4</td>
<td>D5</td>
<td>D6</td>
</tr>
<tr>
<td>6</td>
<td>D7</td>
<td>D8</td>
<td>D9</td>
</tr>
<tr>
<td>7</td>
<td>D10</td>
<td>D11</td>
<td>D12</td>
</tr>
<tr>
<td>8</td>
<td>S1/Z1</td>
<td>M0 or M1/Z2</td>
<td>E2</td>
</tr>
<tr>
<td>9</td>
<td></td>
<td></td>
<td>J1</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>B3</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>C2</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>H4</td>
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<tr>
<td></td>
<td></td>
<td></td>
<td>G1</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>F2</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Z3</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Z4</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>Z5</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Framing bytes</th>
<th>framing bytes— States the beginning of the STS-1 frame.</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>J0</td>
</tr>
<tr>
<td></td>
<td>section trace (J0)/ section growth (Z0)— section trace byte or section growth byte</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Transport Overhead</th>
<th>Path Overhead</th>
</tr>
</thead>
<tbody>
<tr>
<td>framing bytes</td>
<td>framing bytes— States the beginning of the STS-1 frame.</td>
</tr>
<tr>
<td>framing bytes</td>
<td>framing bytes— States the beginning of the STS-1 frame.</td>
</tr>
</tbody>
</table>

- **B1** section bit-interleaved parity code (BIP-8 byte)—Parity code (even parity), used to detect transmission errors on this section. It is evaluated on the previous frame after the scrambling operation.
- **J0** section trace (J0)/section growth (Z0)—section trace byte or section growth byte
- **E1** section orderwire byte—64Kbit/s digital channel to transport a voice signal between operators at the section endpoints.
- **F1** section user channel byte—not defined
- **D1, D2, D3** section data communications channel (DCC) bytes—192Kbit/s channel used for OAM&P.
SONET Overheads

Line Overhead:
- Generated and removed by the line terminating equipment (LTE)
- VT identification in a frame
- Multiplexing/routing
- Performance monitoring
- Protection switching
- Line management

STS Path Overhead:
- Generated and removed by Path Terminal Equipment (PTE)
- end-to-end monitoring of VT/SPE
- Connection management
### SONET overhead - LOH

**Section Overhead**

<table>
<thead>
<tr>
<th>1</th>
<th>2</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>A1</td>
<td>A2</td>
<td>J0/Z0</td>
</tr>
<tr>
<td>B1</td>
<td>E1</td>
<td>F1</td>
</tr>
<tr>
<td>D1</td>
<td>D2</td>
<td>D3</td>
</tr>
<tr>
<td>H1</td>
<td>H2</td>
<td>H3</td>
</tr>
<tr>
<td>B2</td>
<td>K1</td>
<td>K2</td>
</tr>
<tr>
<td>D4</td>
<td>D5</td>
<td>D6</td>
</tr>
<tr>
<td>D7</td>
<td>D8</td>
<td>D9</td>
</tr>
<tr>
<td>D10</td>
<td>D11</td>
<td>D12</td>
</tr>
<tr>
<td>S1/Z1</td>
<td>M0 or M1/Z2</td>
<td>E2</td>
</tr>
</tbody>
</table>

**Line Overhead**

<table>
<thead>
<tr>
<th>1</th>
<th>2</th>
<th>3</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>J1</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>B3</td>
<td></td>
</tr>
<tr>
<td>3</td>
<td>C2</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>H4</td>
<td></td>
</tr>
<tr>
<td>5</td>
<td>G1</td>
<td></td>
</tr>
<tr>
<td>6</td>
<td>F2</td>
<td></td>
</tr>
<tr>
<td>7</td>
<td>Z3</td>
<td></td>
</tr>
<tr>
<td>8</td>
<td>Z4</td>
<td></td>
</tr>
<tr>
<td>9</td>
<td>Z5</td>
<td></td>
</tr>
</tbody>
</table>

#### K1, K2
- **automatic protection switching (APS channel) bytes** — signaling to face fault management

#### D4, D12
- **line data communications channel (DCC) bytes** — 9 bytes for a 576Kbit/s management channel to carry over O&M operations

#### S1
- **synchronization status (S1)** — Used to carry global network-wide synchronization.

#### Z1
- **growth (Z1)** — not defined

#### M0
- **STS-1 REI-L (M0)** — to carry signaling in case of remote error indication

#### M1
- **STS-N REI-L (M1)** — to perform restoration operation

#### Z2
- **growth (Z2)** — not used

#### E2
- **orderwire byte** — 64Kbit/s digital channel to transport a voice signal between operators at the section endpoints

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**H1**
- **STS payload pointer (H1 and H2)** — stores the pointer. It is the offset between the first payload byte and the actual VT first byte.

**H2**
- **pointer action byte (H3)** — used when a negative stuffing is performed. It stores the additional byte of the last frame.

**H3**
- **line bit-interleaved parity code (BIP-8) byte** — parity check code, used to detect errors at the line layer
SONET pointers

How to manage de-synchronization among apparatus clocks?
- Use pointer to absorb frequency and phase shifting
- They allow to dynamically follow the phase shifting in a simple manner
- And avoid the need of buffering

Bit Stuffing was used in PDH. Byte stuffing is used in SONET
- When the SPE speed is smaller than STS-1 speed, an extra byte is inserted
- When the SPE speed is larger than STS-1 speed, an extra byte is removed and transmitted in the overhead
Positive stuffing

SPE slower than STS-1

- Periodically, when the SPE has a delay of 1 byte, odd bits of pointers are negated, to signal a positive stuffing operation.
- An additional byte is added in the VT, allowing it to be delayed by 1 byte.
- The additional byte is always put close to the H3 header field.
- The pointer is then incremented by 1 in the next frame, and following frames will hold the new value.

<table>
<thead>
<tr>
<th>Frame N</th>
<th>Frame N+1</th>
<th>Frame N+2</th>
<th>Frame N+3</th>
</tr>
</thead>
<tbody>
<tr>
<td>H1</td>
<td>H1</td>
<td>H1</td>
<td>H1</td>
</tr>
<tr>
<td>H2</td>
<td>H2</td>
<td>H2</td>
<td>H2</td>
</tr>
<tr>
<td>H3</td>
<td>H3</td>
<td>H3</td>
<td>H3</td>
</tr>
<tr>
<td>P</td>
<td>P</td>
<td>P+1</td>
<td>P+1</td>
</tr>
</tbody>
</table>

500 µs elapsed

Extra bytes allow the SPE to slip back in time.
A positive stuff byte immediately follows the H3 byte.
Negative stuffing

SPE faster then the STS-1

- Periodically, when the SPE has an additional byte, pointer even bits are negated, to signal a negative stuffing operation
- In next frame, the VT starts 1 byte earlier
- The additional byte of the previous VT is put into the H3 header field
- The pointer is then decremented by 1 in the next frame, e following frames will hold the new value.

The SPE moves forward in time when a data byte has been stuffed into the H3 byte.

Actual payload data is written in the H3 bytes.
SDH Framing

- In SDH a different naming scheme is used, but the design is similar to SONET’s.
- Base tributary is STM-1, with a period of 125 $\mu$s.
- Frame has 19440 bits, giving a total speed of 155.520 Mbit/s.
- Information is organized in bytes, using 9 rows of 270 bytes each.
- Virtual container (VC) carried the payload ($261 \times 9 = 2349$ bytes).
- Administrative unit (AU) is the VC plus headers (like the VT).
STM-1 frame in SDH

- **3x90 byte** administrative unit
- **3x3 byte** framing
- **3x87 byte** virtual container

- **0 μs**
- **125 μs**

- overhead
- virtual container

- Time
SONET Network Elements

SONET standard defines several apparatuses to fulfill different functionalities

- Multiplexer and de-multiplexer
- Regenerators
- Add-Drop multiplexers
- Digital cross-connects

All are “electronic” devices, with no elaboration done in the optical domain except transmission
SONET/SDH layering

- Path layer
- Line layer
- Section layer
- Physical layer

Connection

- PTE
- Regenerator
- ADM
- add/drop mux
- PTE
- Path layer
- Line layer
- Section layer
- Physical layer
SONET Network Elements: PTE

Multiplexer and demultiplexer: The main function is mux and demux of tributaries

- Il Path Terminating Element (PTE)
  - Simpler version of multiplexer path-terminating terminal
  - Multiplexes DS–1 channels, and generated the OC-N carrier
  - Two terminal multiplexers connected by a fiber are the simplest SONT topology (section, line, path on the same link)
SONET Network Elements: Regen

Regenerator

- Simplest SONET element. Perform 3R regeneration
- Allows to overcome distance limit at the physical layer
- Receives the input stream, and regenerates the section overhead before retransmitting the frame. Does not modify Line and Path overhead (behaves differently from an Ethernet repeater)
Add-Drop multiplexer: multiplexing and routing over ring topologies

- Multiplexes different tributaries over a single OC–N
- The add/drop operation allows to elaborate, add/drop only signal that must be managed
- Transit traffic is forwarded without the need of particular operation.
- It manages alternate routing in case of fault
**SONET Network Elements: DCS**

Digital cross-connect: multiplexing in general meshed topology
- Different line speed
- Works at the STS-1 granularity
- Used to interconnect several STS-1 inputs
- High-speed cross-connects are used to efficiently mux/demux several channels

![Transparent Switch Matrix](image)
Point-to-point topology

- Simplest topology
- The point-to-point start and end on a PTE, which manages the mux/demux of tributaries
- No routing, and no demux along the path
- Regenerators may be used to avoid transmission problems
Linear add-drop topology

- Still a linear topology
- ADM (and regen) along the line
- ADM allow to add and drop tributaries along the path
- ADM are designed to work in this kind of topologies, which allows a simpler structure than a general cross-connect (there is no need to mux and remux in transit tributaries)
SONET Network Configurations

Hub network setup

- Typically on big aggregation point
- Adopt Digital Cross connect (DCS) working at high rate
- DCSs are much more complex that ADMs: they have to manage both single tributary and SONET stream
SONET Rings

- The most used topology. Can use two or four fibers and an ADM at each node. Bidirectional topology
- Simple protection functions

SONET Ring Architecture
Survivability in SONET
Network Survivability/Fault Management

Survivability: the network uses additional capacity to keep carrying all the traffic when a fault occurs
It’s a must on backbone networks

Protection: “Immediate” and automatic answer of the network to recover from fault

Restoration: typical of complex topologies in which find the network can reconfigure itself slowly (or manually)
SONET adopts several techniques for Survivability, Protection, and Restoration. Typically, they are based on ring topologies that offer two alternating paths.

Nodes close to the fiber fault create a loopback to reconfigure the ring. The topology after the reconfiguration is a monodirectional ring.
1:1 protection

Only nodes close to the fault are involved in the protection.
1+1 protection

Data is transmitted on both paths at the same time.
Every ADM selects from which input to receive data.
Protection and Restoration

Fault recovery is very quick:
- Less than 50ms

Restoration time
- In PDH was in the order of minutes
- In IP networks is takes several minutes
- In Ethernet LANs it takes tens of seconds (up to 60 seconds to reconfigure the spanning tree)